

FIG. 16



MARK-UP COPY

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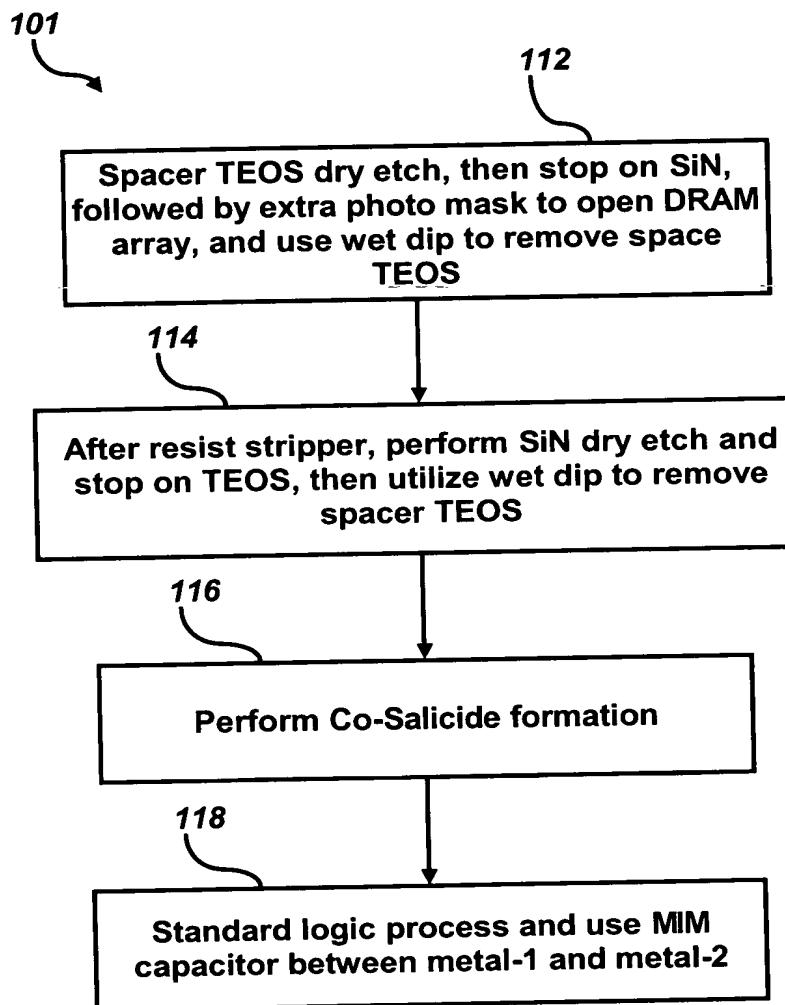


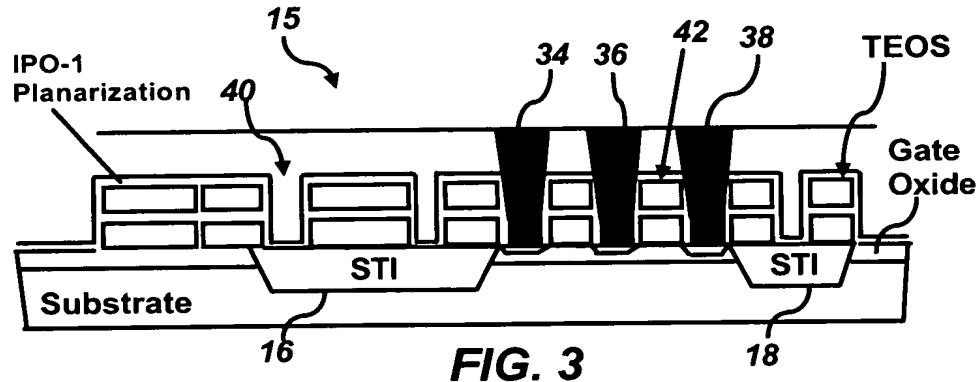
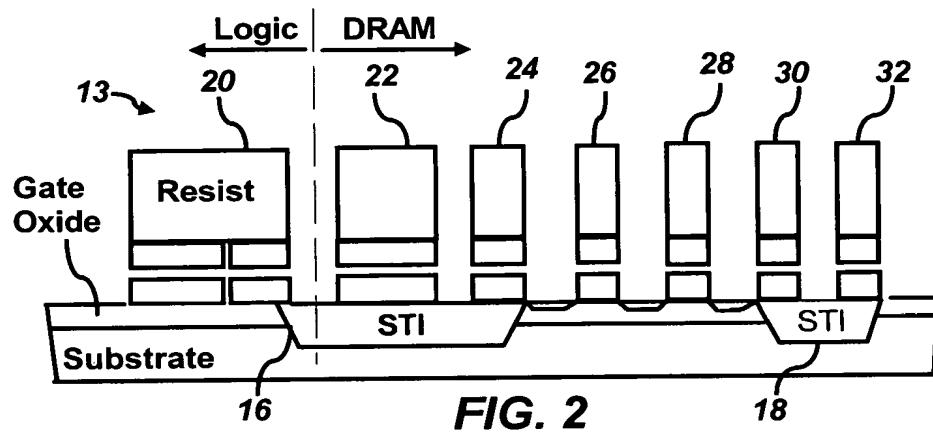
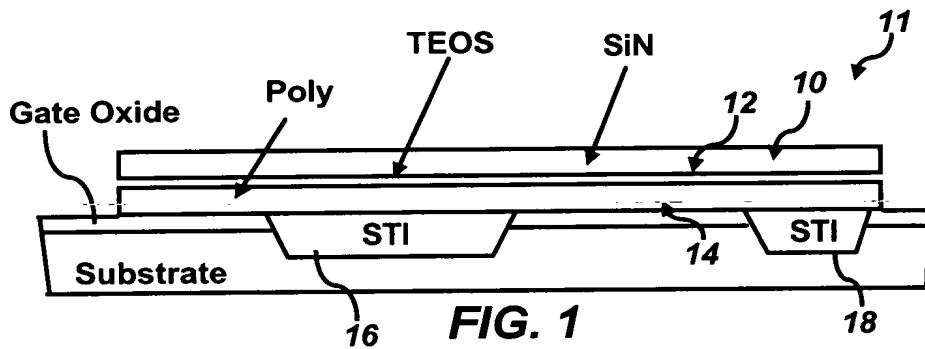
FIG. 17



Inventor: Min-Hsiung Chiang, et al
Serial No.: 09/975,840 Filed: 10/11/2001
For: Methods And Systems For Forming Embedded DRAM For An MIM Capacitor
Attorney Doc. No.: 67,200-547

REPLACEMENT SHEET

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REPLACEMENT SHEET

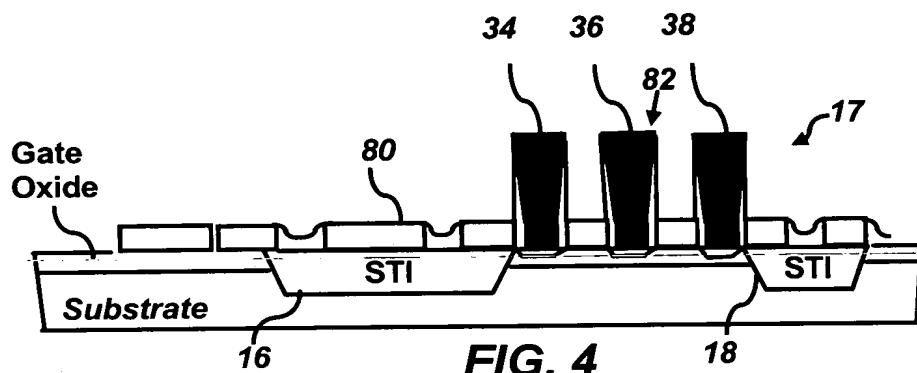


FIG. 4

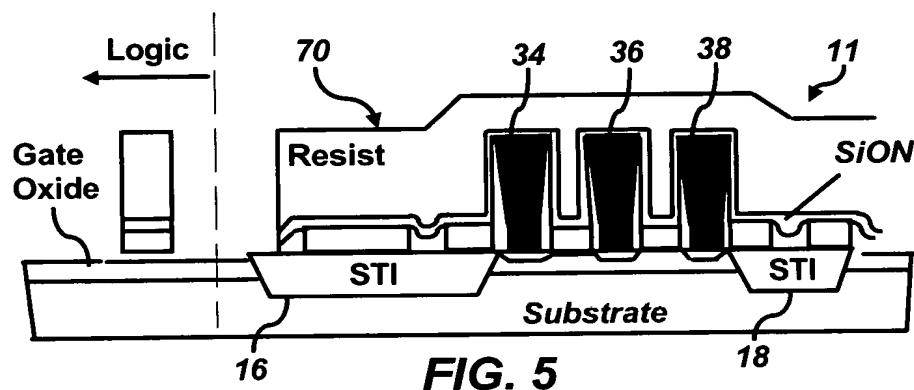


FIG. 5

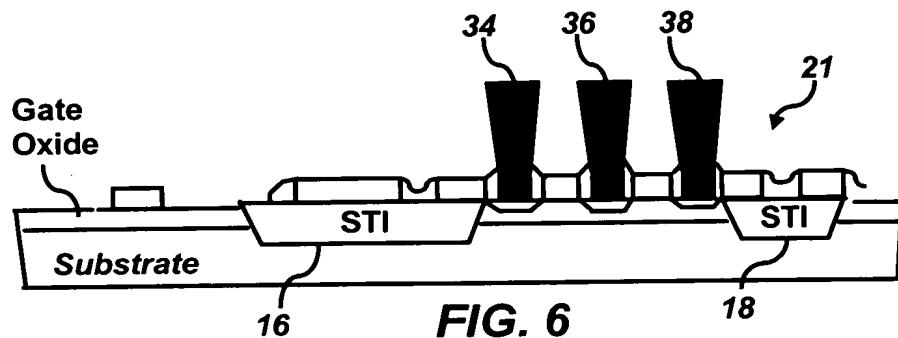


FIG. 6

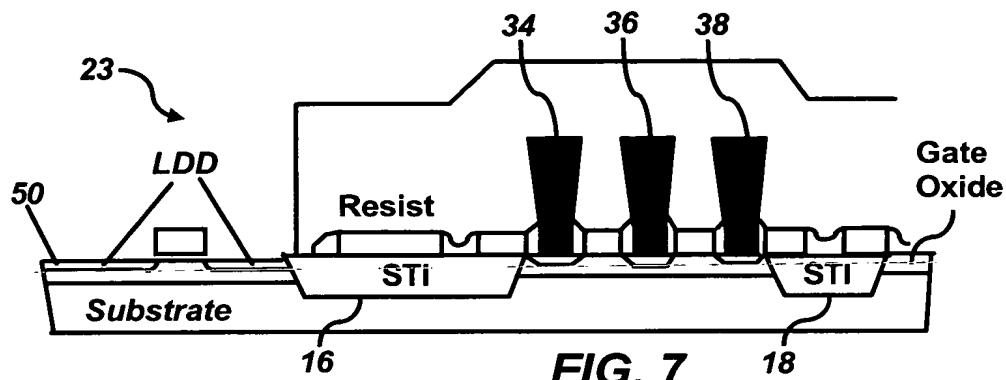


FIG. 7

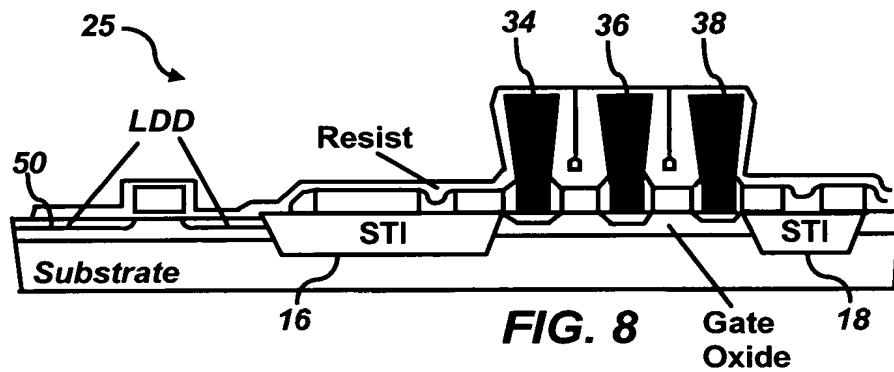


FIG. 8

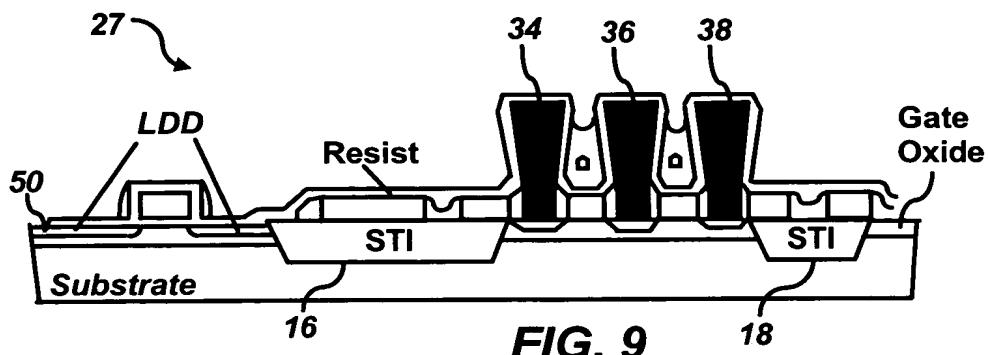


FIG. 9



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REPLACEMENT SHEET

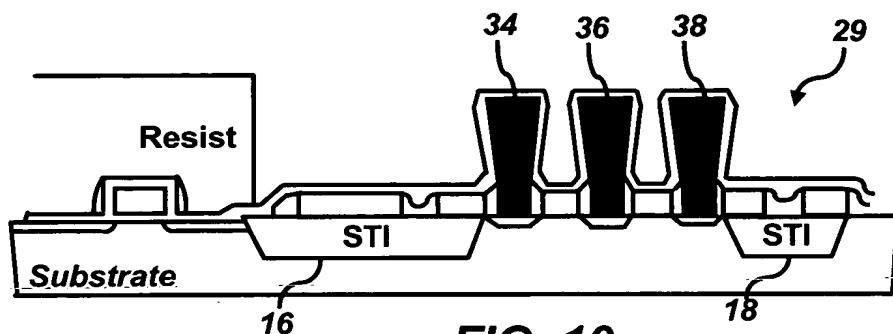


FIG. 10

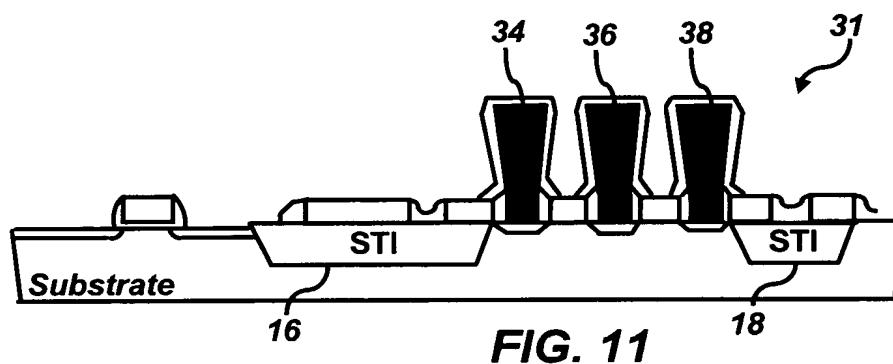


FIG. 11

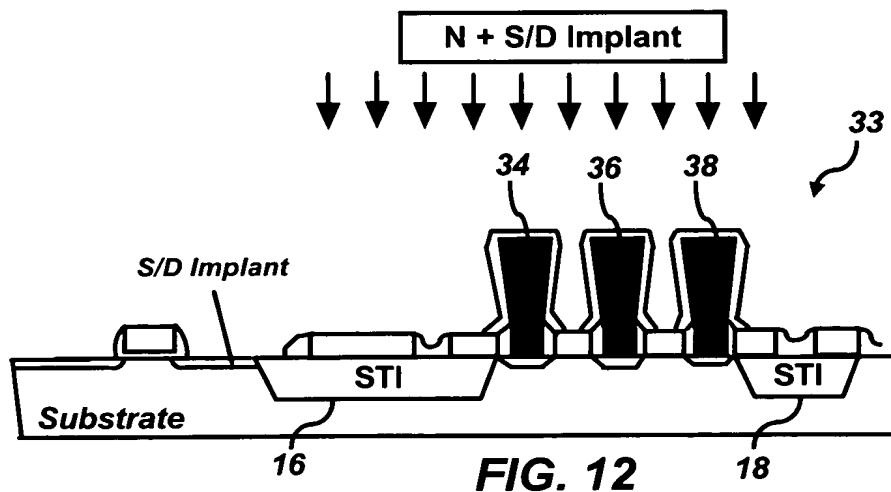


FIG. 12

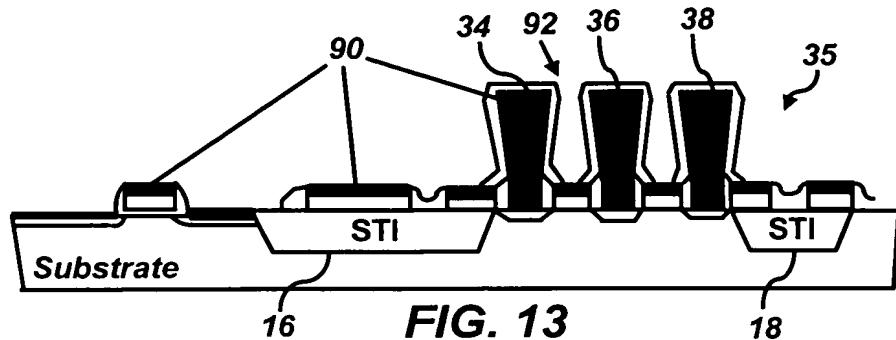


FIG. 13

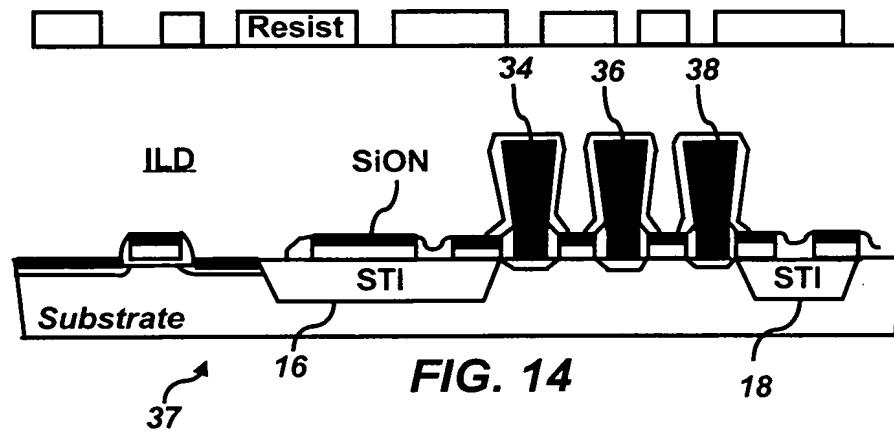


FIG. 14

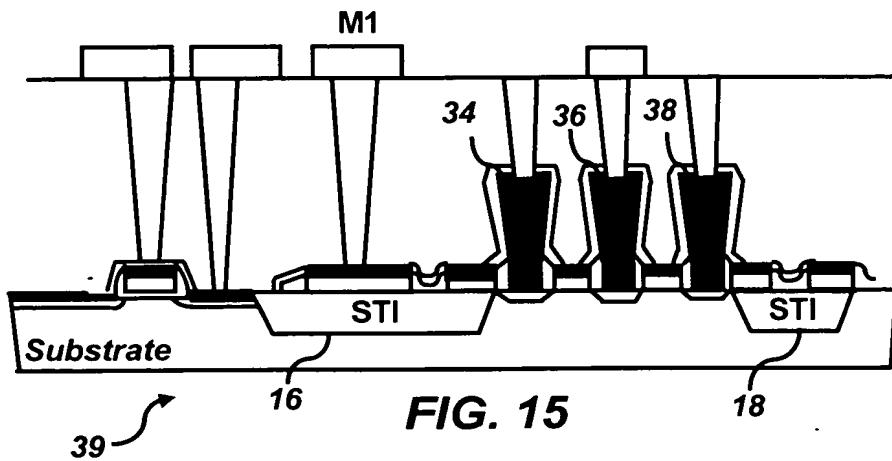


FIG. 15

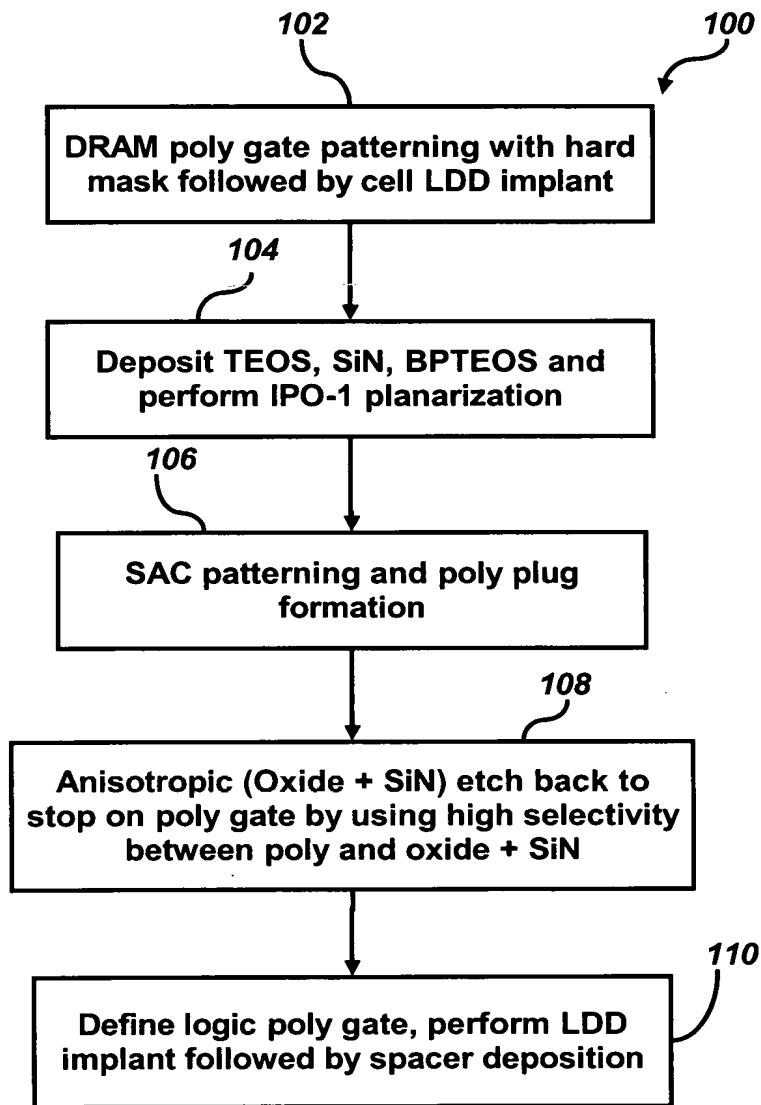


FIG. 16

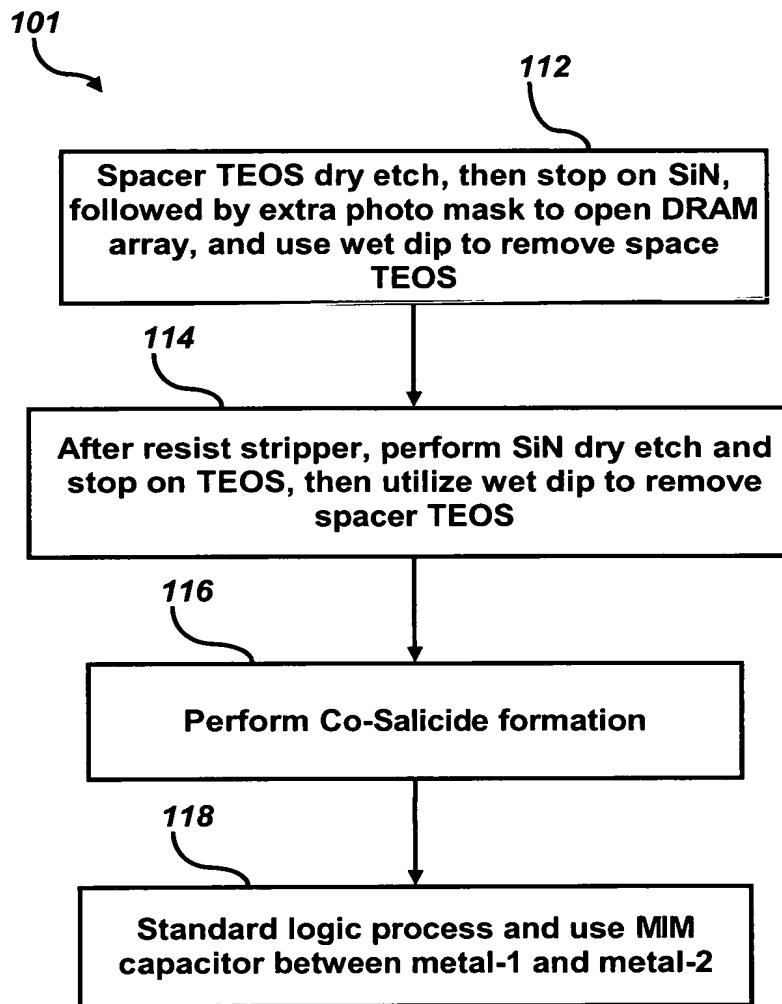


FIG. 17